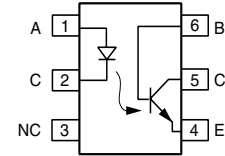
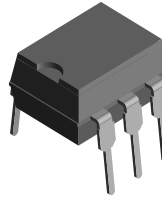


## Optocoupler, Phototransistor Output, With Base Connection

### Features

- Isolation Test Voltage (1.0 s), 5300 V<sub>RMS</sub>
- V<sub>CEsat</sub> 0.25 (≤ 0.4) V, I<sub>F</sub> = 10 mA, I<sub>C</sub> = 2.5 mA
- Built to conform to VDE Requirements
- Highest Quality Premium Device
- Long Term Stability
- Storage Temperature, - 55 ° to + 150 °C
- Lead (Pb)-free component
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC



1179004

### Agency Approvals

- UL1577, File No. E52744 System Code H or J, Double Protection
- DIN EN 60747-5-2 (VDE0884)  
DIN EN 60747-5-5 pending  
Available with Option 1
- CSA 93751
- BSI IEC60950 IEC60065

### Description

The SFH601 is an optocoupler with a Gallium Arsenide LED emitter which is optically coupled with a silicon planar phototransistor detector. The component is packaged in a plastic plug-in case 20 AB DIN 41866.

The coupler transmits signals between two electrically isolated circuits.

### Order Information

Part	Remarks
SFH601-1	CTR 40 - 80 %, DIP-6
SFH601-2	CTR 63 - 125 %, DIP-6
SFH601-3	CTR 100 - 200 %, DIP-6
SFH601-4	CTR 160 - 320 %, DIP-6
SFH601-1X006	CTR 40 - 80 %, DIP-6 400 mil (option 6)
SFH601-1X007	CTR 40 - 80 %, SMD-6 (option 7)
SFH601-1X009	CTR 40 - 80 %, SMD-6 (option 9)
SFH601-2X006	CTR 63 - 125 %, DIP-6 400 mil (option 6)
SFH601-2X007	CTR 63 - 125 %, SMD-6 (option 7)
SFH601-2X009	CTR 63 - 125 %, SMD-6 (option 9)
SFH601-3X006	CTR 100 - 200 %, DIP-6 400 mil (option 6)
SFH601-3X007	CTR 100 - 200 %, SMD-6 (option 7)
SFH601-3X009	CTR 100 - 200 %, SMD-6 (option 9)
SFH601-4X006	CTR 160 - 320 %, DIP-6 400 mil (option 6)
SFH601-4X007	CTR 160 - 320 %, SMD-6 (option 7)
SFH601-4X009	CTR 160 - 320 %, SMD-6 (option 9)

For additional information on the available options refer to Option Information.

### Absolute Maximum Ratings

$T_{amb} = 25\text{ °C}$ , unless otherwise specified

Stresses in excess of the absolute Maximum Ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute Maximum Rating for extended periods of the time can adversely affect reliability.

### Input

Parameter	Test condition	Symbol	Value	Unit
Reverse voltage		$V_R$	6.0	V
DC forward current		$I_F$	60	mA
Surge forward current	$t = 10\ \mu\text{s}$	$I_{FSM}$	2.5	A
Total power dissipation		$P_{diss}$	100	mW

### Output

Parameter	Test condition	Symbol	Value	Unit
Collector-emitter voltage		$V_{CE}$	100	V
Emitter-base voltage		$V_{EBO}$	7.0	V
Collector current		$I_C$	50	mA
	$t = 1.0\ \text{ms}$	$I_C$	100	mA
Power dissipation		$P_{diss}$	150	mW

### Coupler

Parameter	Test condition	Symbol	Value	Unit
Isolation test voltage <sup>1)</sup>	$t = 1.0\ \text{s}$	$V_{ISO}$	5300	$V_{RMS}$
Creepage			$\geq 7.0$	mm
Clearance			$\geq 7.0$	mm
Isolation thickness between emitter and detector			$\geq 0.4$	mm
Comparative tracking <sup>2)</sup>			175	
Isolation resistance	$V_{IO} = 500\ \text{V}, T_{amb} = 25\text{ °C}$	$R_{IO}$	$\geq 10^{12}$	$\Omega$
	$V_{IO} = 500\ \text{V}, T_{amb} = 100\text{ °C}$	$R_{IO}$	$\geq 10^{11}$	$\Omega$
Storage temperature range		$T_{stg}$	- 55 to + 150	$^{\circ}\text{C}$
Ambient temperature range		$T_{amb}$	- 55 to + 100	$^{\circ}\text{C}$
Junction temperature		$T_j$	100	$^{\circ}\text{C}$
Soldering temperature	max. 10 s, dip soldering: distance to seating plane $\geq 1.5\ \text{mm}$	$T_{sld}$	260	$^{\circ}\text{C}$

<sup>1)</sup> between emitter and detector referred to climate DIN 40046, part 2, Nov. 74

<sup>2)</sup> index per DIN IEC 60112/VDE0303, part 1



### Electrical Characteristics

T<sub>amb</sub> = 25 °C, unless otherwise specified

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

### Input

Parameter	Test condition	Symbol	Min	Typ.	Max	Unit
Forward voltage	I <sub>F</sub> = 60 mA	V <sub>F</sub>		1.25	1.65	V
Breakdown voltage	I <sub>R</sub> = 10 μA	V <sub>BR</sub>	6.0			V
Reverse current	V <sub>R</sub> = 6.0 V	I <sub>R</sub>		0.01	10	μA
Capacitance	V <sub>F</sub> = 0 V, f = 1.0 MHz	C <sub>O</sub>		25		pF
Thermal resistance		R <sub>thja</sub>		750		K/W

### Output

Parameter	Test condition	Part	Symbol	Min	Typ.	Max	Unit
Collector-emitter capacitance	f = 1.0 MHz, V <sub>CE</sub> = 5.0 V		C <sub>CE</sub>		6.8		pF
Collector - base capacitance	f = 1.0 MHz, V <sub>CB</sub> = 5.0 V		C <sub>CB</sub>		8.5		pF
Emitter - base capacitance	f = 1.0 MHz, V <sub>EB</sub> = 5.0 V		C <sub>EB</sub>		11		pF
Thermal resistance			R <sub>THJamb</sub>		500		K/W
Collector-emitter leakage current	V <sub>CE</sub> = 10 V	SFH601-1	I <sub>CEO</sub>		2.0	50	nA
		SFH601-2	I <sub>CEO</sub>		2.0	50	nA
		SFH601-3	I <sub>CEO</sub>		5.0	100	nA
		SFH601-4	I <sub>CEO</sub>		5.0	100	nA

### Coupler

Parameter	Test condition	Symbol	Min	Typ.	Max	Unit
Saturation voltage, collector-emitter	I <sub>F</sub> = 10 mA, I <sub>C</sub> = 2.5 mA	V <sub>CEsat</sub>		0.25	0.4	V
Capacitance (input-output)	V <sub>I-O</sub> = 0, f = 1.0 MHz	C <sub>IO</sub>		0.6		pF

### Current Transfer Ratio

Current Transfer Ratio and Collector-Emitter Leakage Current by Dash Number

Parameter	Test condition	Part	Symbol	Min	Typ.	Max	Unit
I <sub>C</sub> /I <sub>F</sub> at V <sub>CE</sub> = 5.0 V	I <sub>F</sub> = 10 mA	SFH601-1	CTR	40		80	%
		SFH601-2	CTR	63		125	%
		SFH601-3	CTR	100		200	%
		SFH601-4	CTR	160		320	%
	I <sub>F</sub> = 1.0 mA	SFH601-1	CTR	13	30		%
		SFH601-2	CTR	22	45		%
		SFH601-3	CTR	34	70		%
		SFH601-4	CTR	56	90		%

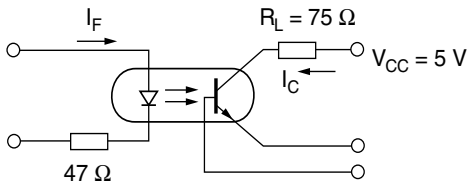
## Switching Non-saturated

Parameter	Current	Rise time	Fall time	Turn-on time	Turn-off time
Test condition	$V_{CC} = 5.0\text{ V}, R_L = 75\ \Omega$				
Symbol	$I_F$	$t_r$	$t_f$	$t_{on}$	$t_{off}$
Unit	mA	$\mu\text{s}$		$\mu\text{s}$	$\mu\text{s}$
	10	2.0	2.0	3.0	2.3

## Switching Saturated

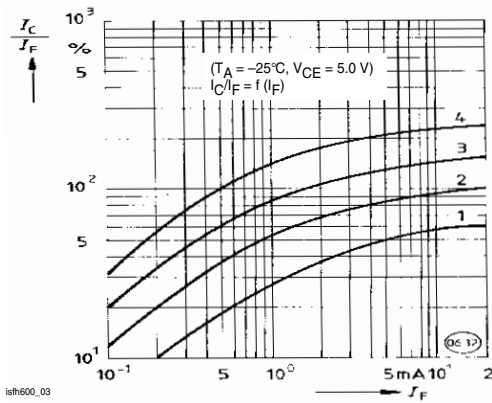
Parameter	Current	Rise time	Fall time	Turn-on time	Turn-off time
Test condition	$V_{CEsat} = 0.25 (\leq 0.4)\text{ V}$				
Symbol	$I_F$	$t_r$	$t_f$	$t_{on}$	$t_{off}$
Unit	mA	$\mu\text{s}$	$\mu\text{s}$	$\mu\text{s}$	$\mu\text{s}$
SFH601-1	20	2.0	11	3.0	18
SFH601-2	10	3.0	14	4.2	23
SFH601-3	10	3.0	14	4.2	23
SFH601-4	0.5	4.6	15	6.0	25

## Typical Characteristics ( $T_{amb} = 25\text{ }^\circ\text{C}$ unless otherwise specified)



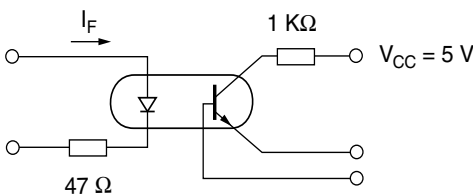
ish601\_01

Figure 1. Linear Operation ( without Saturation)



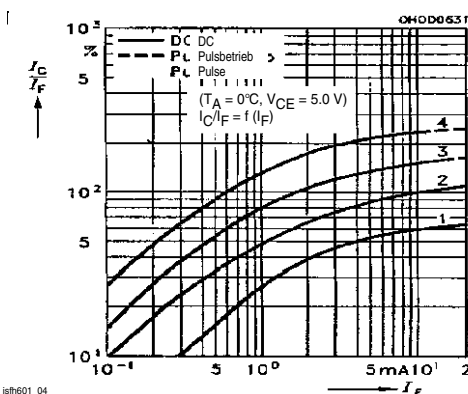
ish600\_03

Figure 3. Current Transfer Ratio vs. Diode Current



ish601\_02

Figure 2. Switching Operation (with Saturation)



ish601\_04

Figure 4. Current Transfer Ratio vs. Diode Current

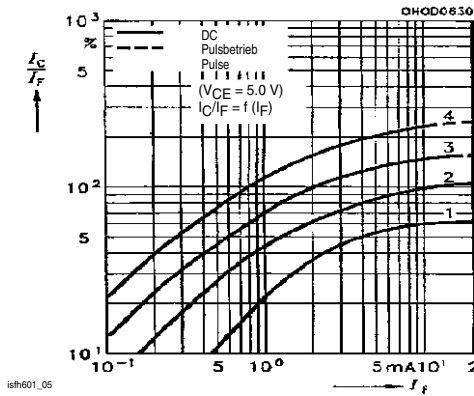


Figure 5. Current Transfer Ratio vs. Diode Current

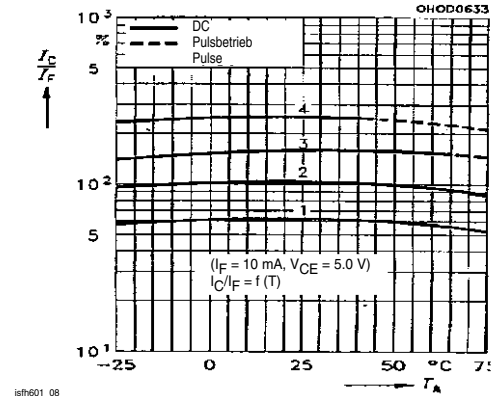


Figure 8. Current Transfer Ratio vs. Diode Current

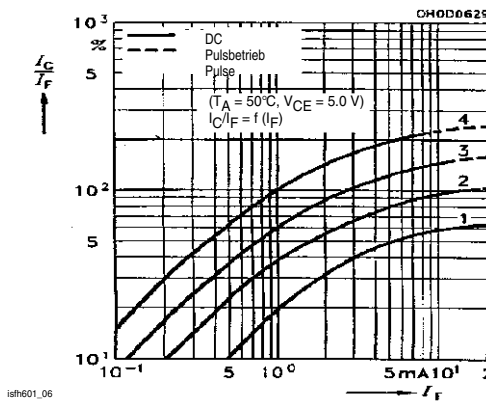


Figure 6. Current Transfer Ratio vs. Diode Current

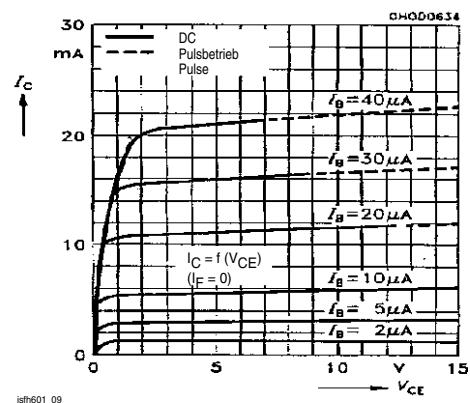


Figure 9. Transistor Characteristics

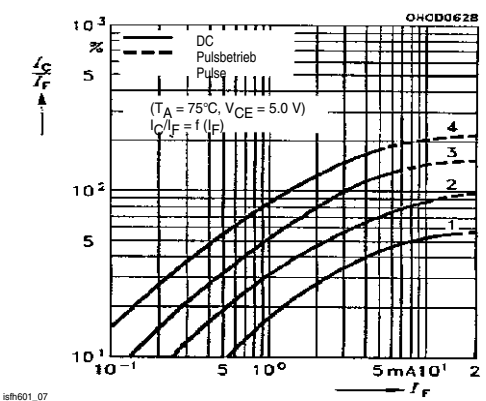


Figure 7. Current Transfer Ratio vs. Diode Current

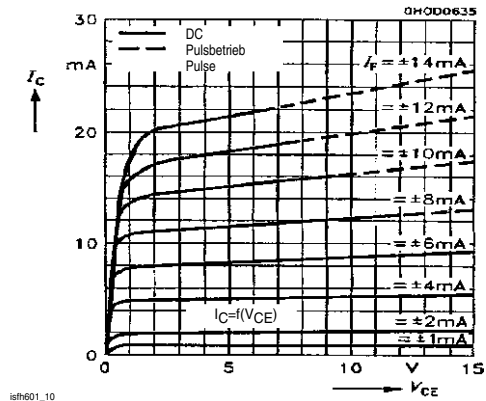


Figure 10. Output Characteristics

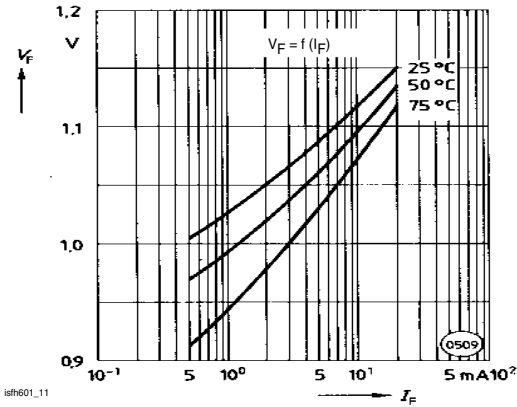


Figure 11. Forward Voltage

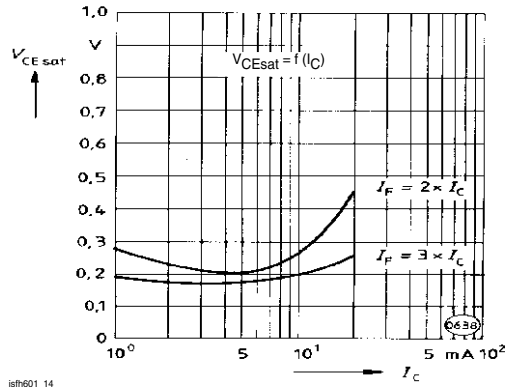


Figure 14. Saturation Voltage vs. Collector Current and Modulation Depth SFH601-2

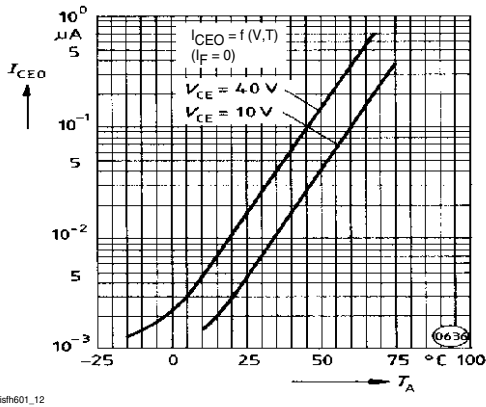


Figure 12. Collector-Emitter Off-state Current

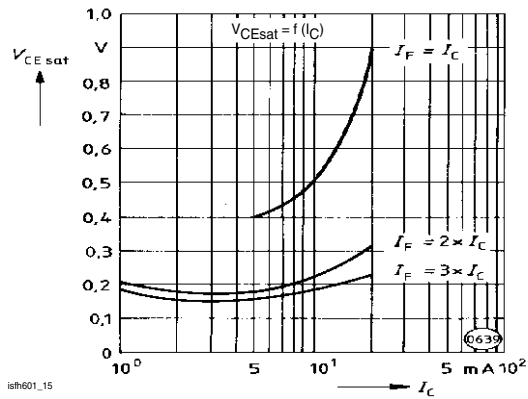


Figure 15. Saturation Voltage vs. Collector Current and Modulation Depth SFH601-3

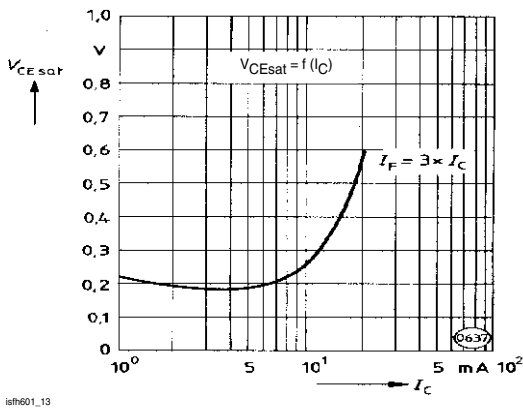


Figure 13. Saturation Voltage vs. Collector Current and Modulation Depth SFH601-1

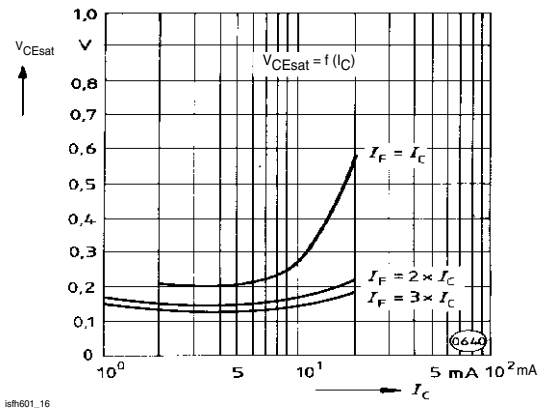


Figure 16. Saturation Voltage vs. Collector Current and Modulation Depth SFH601-4

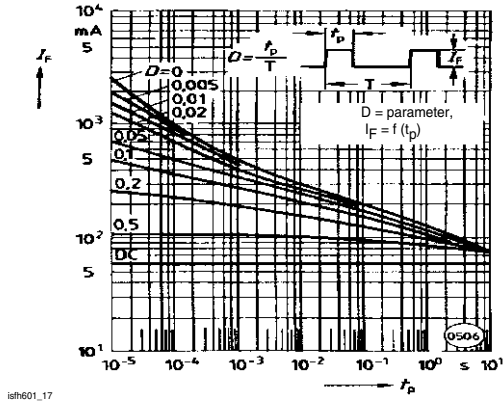


Figure 17. Permissible Pulse Load

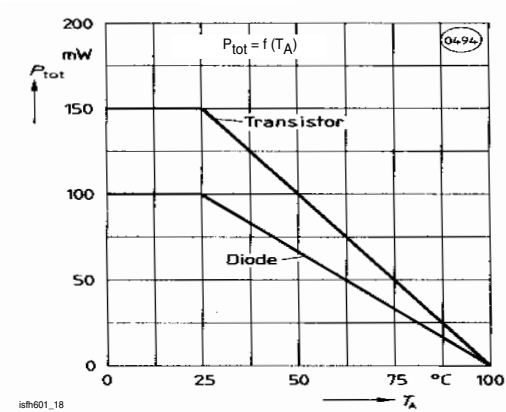


Figure 18. Permissible Power Dissipation for Transistor and Diode

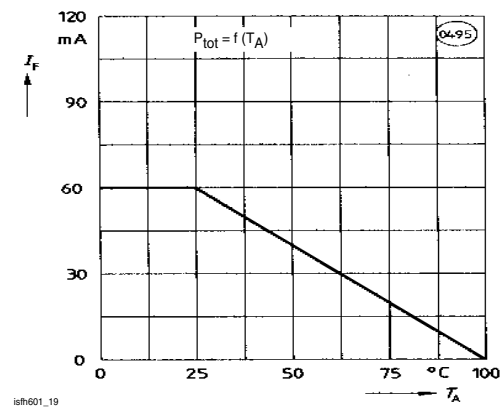
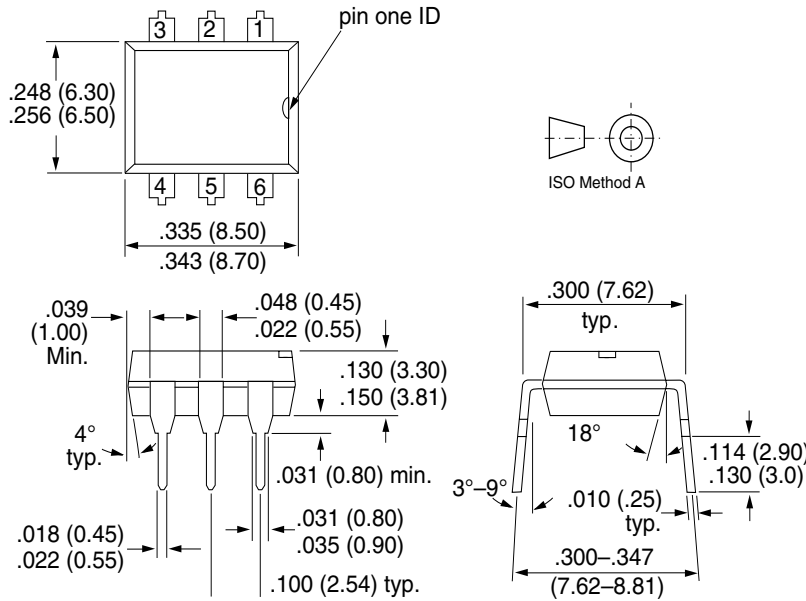


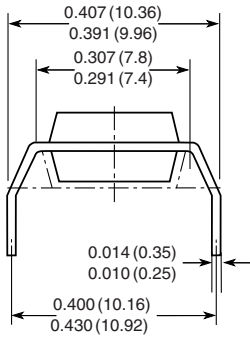
Figure 19. Permissible Forward Current Diode

## Package Dimensions in Inches (mm)

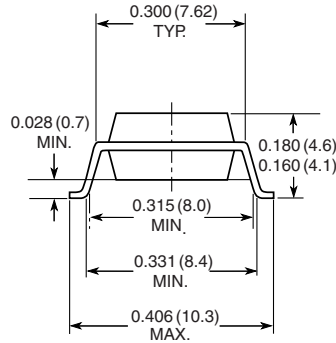


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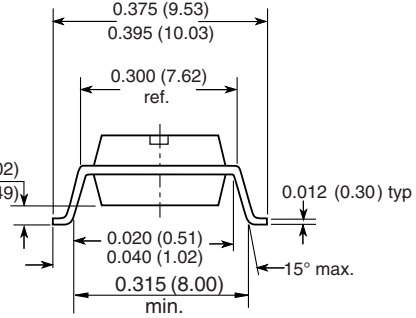
### Option 6



### Option 7



### Option 9



18450





## **Ozone Depleting Substances Policy Statement**

It is the policy of Vishay Semiconductor GmbH to

1. Meet all present and future national and international statutory requirements.
2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

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Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

Vishay Semiconductor GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany



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