

**Vishay Semiconductors** 

## Optocoupler, Phototriac Output, 250 V $V_{DRM}$

### Features

- Isolation materials according to UL 94-VO
- Pollution degree 2 (DIN/VDE 0110 resp. IEC 60664)
- Climatic classification 55/100/21 (IEC 60068 part 1)
- Special construction: Therefore, extra low coupling capacity of typical 0.2 pF, high Common Mode Rejection
- I<sub>FT</sub> offered in 3 groups
- Rated impulse voltage (transient overvoltage)
  V<sub>IOTM</sub> = 6 kV peak
- Isolation test voltage (partial discharge test )  $V_{pd} = 1.6 \text{ kV}$
- Creepage current resistance according to VDE 0303/IEC 60112 Comparative Tracking Index: CTI = 275
- Thickness through insulation  $\ge 0.75$  mm
- · Lead-free component
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

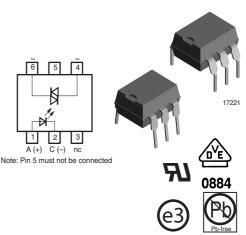
### Agency Approvals

- UL1577, File No. E76222 System Code C, Double Protection
- BSI: BS EN 41003, BS EN 60095 (BS 415), BS EN 60950 (BS 7002), Certificate number 7081 and 7402
- DIN EN 60747-5-2 (VDE0884) DIN EN 60747-5-5 pending
- FIMKO (SETI): EN 60950, Certificate No. 12398

## Applications

Monitors Air conditioners Line Switches Solid state relay Microwave Circuits for safe protective separation against electrical shock according to safety class II (reinforced isolation):

• For appl. class I - IV at mains voltage  $\leq 300~V$ 



• For appl. class I - III at mains voltage  $\leq 600$  V according to DIN EN 60747-5-2(VDE0884)/ DIN EN 60747-5-5 pending, table 2, suitable for:

Monitors, air conditioners, line switches, solid state relays, microwaves.

### Description

The K3010P/ K3010PG series consists of a phototransistor optically coupled to a gallium arsenide infrared-emitting diode in a 6-pin plastic dual inline package.

#### **VDE Standards**

These couplers perform safety functions according to the following equipment standards:

## DIN EN 60747-5-2(VDE0884)/ DIN EN 60747-5-5 pending

Optocoupler for electrical safety requirements IEC 60950/EN 60950

Office machines (applied for reinforced isolation for mains voltage  $^{\leq}$  400 VRMS)

## VDE 0804

Telecommunication apparatus and data processing **IEC 60065** 

Safety for mains-operated electronic and related household apparatus

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### **Order Information**

Part	Remarks
K3010P	15 mA, I <sub>FT</sub> , V <sub>DRM</sub> = 250 V, DIP-6 300 mil
K3011P	10 mA, I <sub>FT</sub> , V <sub>DRM</sub> = 250 V, DIP-6 300 mil
K3012P	5 mA, I <sub>FT</sub> , V <sub>DRM</sub> = 250 V, DIP-6 300 mil
K3010PG	15 mA, I <sub>FT</sub> , V <sub>DRM</sub> = 250 V, DIP-6 400 mil
K3011PG	10 mA, I <sub>FT</sub> , V <sub>DRM</sub> = 250 V, DIP-6 400 mil
K3012PG	5 mA, I <sub>FT</sub> , V <sub>DRM</sub> = 250 V, DIP-6 400 mil

G = Leadform 10.16 mm; G is not marked on the body

## **Absolute Maximum Ratings**

 $T_{amb}$  = 25 °C, unless otherwise specified

Stresses in excess of the absolute Maximum Ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute Maximum Rating for extended periods of the time can adversely affect reliability.

#### Input

Parameter	Test condition	Symbol	Value	Unit
Reverse voltage		V <sub>R</sub>	5	V
Forward current		١ <sub>F</sub>	80	mA
Forward surge current	$t_p \le 10 \ \mu s$	I <sub>FSM</sub>	3	A
Power dissipation		P <sub>diss</sub>	100	mW
Junction temperature		Tj	100	C°

#### Output

Parameter	Test condition	Symbol	Value	Unit
Off state output terminal voltage		V <sub>DRM</sub>	250	V
On state RMS current		I <sub>TRM</sub>	100	mA
Peak surge current, non- repetitive	$t_p \le 10 \text{ ms}$	I <sub>TMS</sub>	1.5	A
Power dissipation		P <sub>diss</sub>	300	mW
Junction temperature		Tj	100	°C

### Coupler

Parameter	Test condition	Symbol	Value	Unit
Isolation test voltage (RMS)	t = 1 min	V <sub>ISO</sub> <sup>1)</sup>	3750	V <sub>RMS</sub>
Total power dissipation		P <sub>tot</sub>	350	mW
Ambient temperature range		T <sub>amb</sub>	- 40 to + 85	°C
Storage temperature range		T <sub>stg</sub>	- 55 to + 100	°C
Soldering temperature	2 mm from case, t $\leq$ 10 s	T <sub>sld</sub>	260	°C

<sup>1)</sup> Related to standard climate 23/50 DIN 50014



### **Vishay Semiconductors**

### **Electrical Characteristics**

 $T_{amb}$  = 25 °C, unless otherwise specified

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

#### Input

Parameter	Test condition	Symbol	Min	Тур.	Max	Unit
Forward voltage	I <sub>F</sub> = 50 mA	V <sub>F</sub>		1.25	1.6	V
Junction capacitance	$V_R = 0, f = 1 MHz$	Cj		50		pF

### Output

Parameter	Test condition	Symbol	Min	Тур.	Max	Unit
Forward peak off-state voltage (repetitive)	I <sub>DRM</sub> = 100 nA	V <sub>DRM</sub> <sup>1)</sup>	250			V
Peak on-state voltage	I <sub>TM</sub> = 100 mA	V <sub>TM</sub>		1.5	3	V
Critical rate of rise of off-state voltage	I <sub>FT</sub> = 0, I <sub>FT</sub> = 30 mA	dV/dt <sub>cr</sub>		10		V/µs
		dV/dt <sub>crq</sub>	0.1	0.2		V/μs

<sup>1)</sup> Test voltage must be applied within dV/dt ratings

### Coupler

Parameter	Test condition	Part	Symbol	Min	Тур.	Max	Unit
Emitting diode trigger current	$V_{S}$ = 3 V, $R_{L}$ = 150 $\Omega$	K3010P	I <sub>FT</sub>		8	15	mA
		K3010PG	I <sub>FT</sub>		8	15	mA
		K3011P	I <sub>FT</sub>		5	10	mA
		K3011PG	I <sub>FT</sub>		5	10	mA
		K3012P	I <sub>FT</sub>		2	5	mA
		K3012PG	I <sub>FT</sub>		2	5	mA
Holding current	$I_F = 10 \text{ mA}, \text{ V}_S \ge 3 \text{ V}$		Ι <sub>Η</sub>		100		μA

Note:  $\mathbf{I}_{\text{FT}}$  is defined as a minimum trigger current

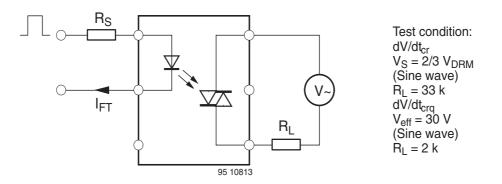
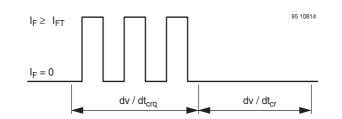


Figure 1. Test circuit for dV/dt<sub>cr</sub> and dV/dt<sub>crq</sub>

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dv/dt<sub>cr</sub> Highest value of the "rate of rise of off-state voltage" which does not cause any switching from the off-state to the on-state

dv/dt<sub>crq</sub> Highest value of the "rate of rise of communicating voltage" which does not switch on the device again, after the voltage has decreased to zero and the trigger current is switched from I<sub>FT</sub> to zero

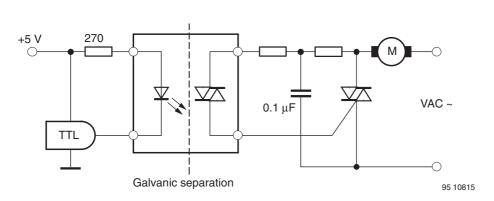


Figure 3. Motor control circuit

Figure 2.

## **Maximum Safety Ratings**

(according to DIN EN 60747-5-2(VDE0884)/ DIN EN 60747-5-5 pending) see figure 1 This optocoupler is suitable for safe electrical isolation only within the safety ratings. Compliance with the safety ratings shall be ensured by means of suitable protective circuits.

#### Input

Parameter	Test condition	Symbol	Min	Тур.	Max	Unit
Forward current		I <sub>S, INPUT</sub>			130	mA

#### Output

Parameter	Test condition	Symbol	Min	Тур.	Max	Unit
Power dissipation		P <sub>S, OUTPUT</sub>			600	mW



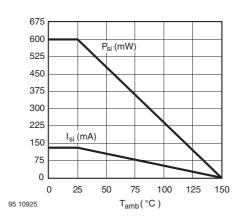
## **Vishay Semiconductors**

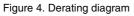
### Coupler

Parameter	Test condition	Symbol	Min	Тур.	Max	Unit
Rated transient voltage		V <sub>IOTM</sub>			6	kV
Safety temperature		Τ <sub>s</sub>			150	°C
Isolation test voltage		V <sub>IORM</sub>			848	V
		V <sub>IOWM</sub>			600	V <sub>RMS</sub>

## **Insulation Rated Parameters**

Parameter	Test condition	Symbol	Min	Тур.	Max	Unit
Partial discharge test voltage - Routine test	100 %, t <sub>test</sub> = 1 s	V <sub>pd</sub>	1.6			kV
		V <sub>IOTM</sub>	6			kV
Partial discharge test voltage - Lot test (sample test)	$t_{Tr} = 60 \text{ s}, t_{test} = 10 \text{ s}, (see figure 5)$	V <sub>pd</sub>	1.3			kV
Insulation resistance	V <sub>IO</sub> = 500 V	R <sub>IO</sub>	10 <sup>12</sup>			Ω
	$V_{IO}$ = 500 V, $T_{amb}$ = 100 °C	R <sub>IO</sub>	10 <sup>11</sup>			Ω
	$V_{IO}$ = 500 V, $T_{amb}$ = 150 °C	R <sub>IO</sub>	10 <sup>9</sup>			Ω
	(construction test only)					





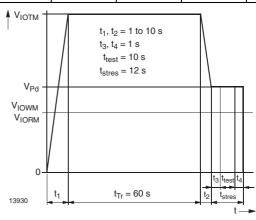


Figure 5. Test pulse diagram for sample test according to DIN EN 60747-5-2(VDE0884)/ DIN EN 60747-; IEC60747

## **Vishay Semiconductors**



## Typical Characteristics (Tamb = 25 °C unless otherwise specified)

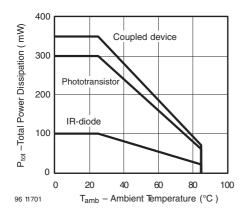


Figure 6. Total Power Dissipation vs. Ambient Temperature

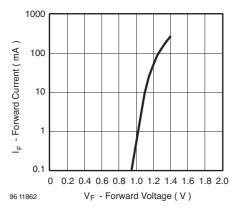
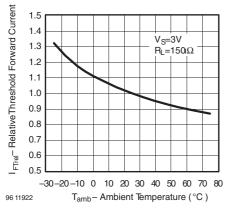


Figure 7. Forward Current vs. Forward Voltage





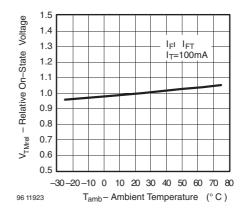


Figure 9. Relative On - State vs. Ambient Temperature

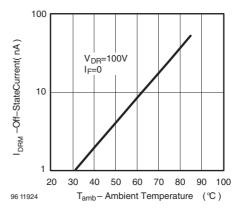


Figure 10. Off - State Current vs. Ambient Temperature

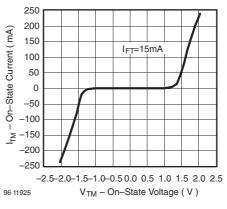


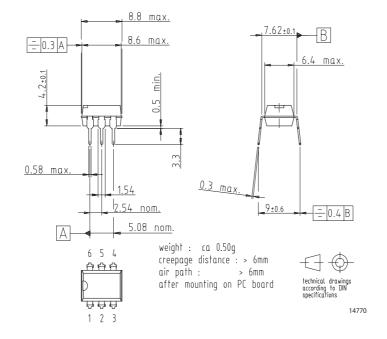
Figure 11. On - State Current vs. Ambient Temperature

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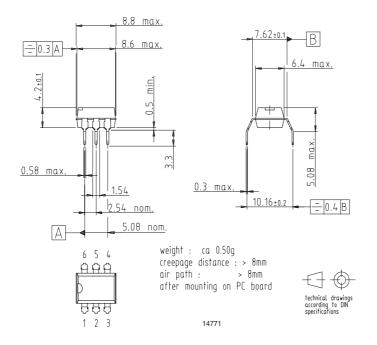


**Vishay Semiconductors** 

## Package Dimensions in mm



## Package Dimensions in mm



## **Vishay Semiconductors**



## **Ozone Depleting Substances Policy Statement**

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operatingsystems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

Vishay Semiconductor GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany Telephone: 49 (0)7131 67 2831, Fax number: 49 (0)7131 67 2423

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