#### Other Information

To obtain the most recent and complete documentation for this demonstration board, please refer to the Microchip web site:

### http://www.microchip.com/graphics

#### **Americas**

Boston - 774-760-0087 Chicago - 630-285-0071 Dallas - 972-818-7423 Detroit - 248-538-2250 Kokomo - 765-864-8360 Los Angeles - 949-462-9523 Phoenix - 480-792-7200 Santa Clara - 408-961-6444 Toronto - 905-673-0699

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01/02/08



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DS51790A

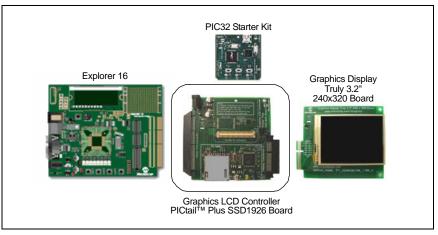


# Graphics LCD Controller PICtail™ Plus SSD1926 Board

#### **Features**

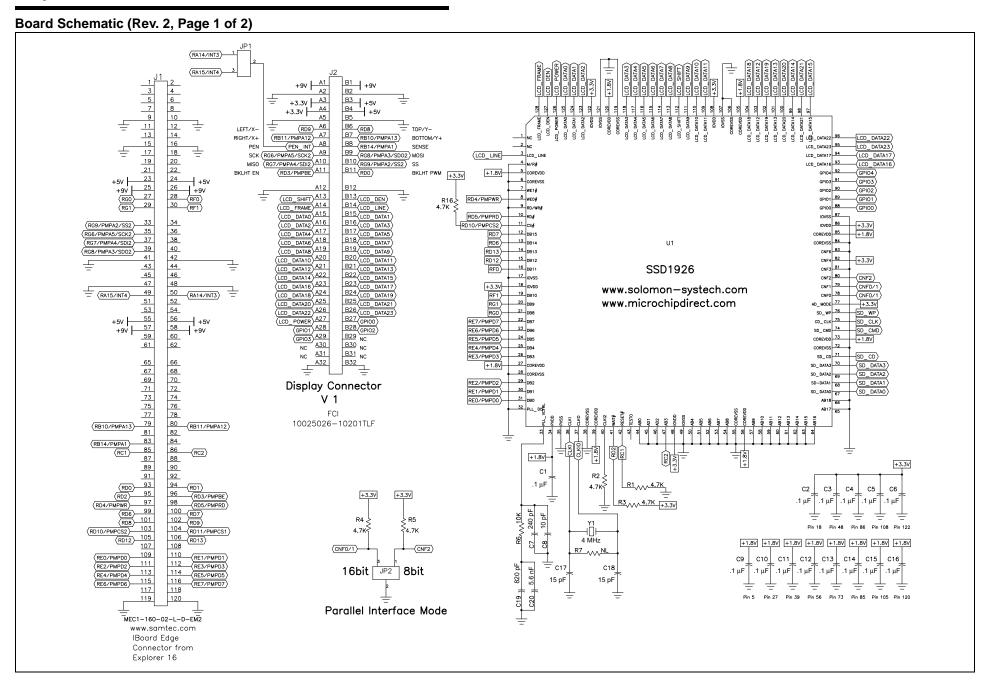
- Graphics display controller, Solomon Systech SSD1926, supporting 4/8-bit STN, 4/8-bit CSTN, 18-bit HR-TFT and 9/12/18/24-bit TFT interfaces. A detailed data sheet can be downloaded from <a href="https://www.solomon-systech.com">www.solomon-systech.com</a>.
  Additional samples can be procured from <a href="https://www.microchipdirect.com">www.microchipdirect.com</a>.
- SD/MMC Card socket, connected to SSD1926 via a 4-wire SD interface
- 16 Megabit (2Mx8) serial Flash memory for additional data storage
- Display connector for interfacing with different display boards
- PICtail™ Plus Interface for connecting to Explorer 16 Development Board
- PIC32 Starter Kit Connector

## **Getting Started**



An Explorer 16 development board (DM240001) or one of the PIC32 Starter Kits (DM320001, DM320003) is required, but only one should be used. An external 9V (AC162039) power supply can be connected through the Explorer 16 or directly to connector, J5. When a PIC32 Starter Kit is used, the setup can be powered via the USB debugger. If your USB is unable to supply enough power, the external power supply should be used. Lastly, a display board, such as the Graphics Display Truly 3.2" 240x320 Board (AC164127-5), should be connected to the display connector. Several board settings can be selected.

- Jumper, JP1, connects an interrupt signal, PEN\_INT, from the Display Connector to either signal, RA14 or RA15 (open by default).
- Jumper, JP2, selects the data bus width for the SSD1926 graphics display controller. In the lower position, the data bus is 8-bit and in the upper position, it is 16-bit (8-bit by default).
- Jumper, JP3, selects the chip select line between RD1 and RD11 for the Flash memory (RD1 by default).



# Graphics LCD Controller PlCtail™ Plus SSD1926 Board

#### Board Schematic (Rev. 2, Page 2 of 2) STARTER KIT CONNECTOR +3.30 +3.3V R9 R10 R11 R12 R13 R14 \$ \$ \$ \$ \$ 4.7K 4.7K 4.7K4.7K4.7K4.7K RE5/PMPD5 RE6/PMPD6 RE4/PMPD4 RE7/PMPD7 RE6/PMPD6 - RG0 - RG1 - RF1 - RF0 - RD12 - RD13 - RD6 - RD7 SD\_DATA3 (RE5/PMPD5) RE4/PMPD4 RD3/PMPBE RD4/PMPWR +3.3V (RE3/PMPD3) (RE3/PMPD3) RE3/PMPD3 RE2/PMPD2 RE1/PMPD1 RE0/PMPD0 (RE2/PMPD2) +3.3V C21 C22 +3.3V RE1/PMPD1 SD\_CLK .1 μF REO/PMPDO SD\_DATA0 (RD5/PMPRD) RD11/PMPCS1 (MT29F2G08AADWP) 10 11 (SD\_CD 12 12 Write (RD3/PMPBE) RD2 RD1 RD0 DM1AA-SF-PEJ(21) FPS009-2203-10 SS110-TP C25 井 C24 C26 47 μF TB1 (RG6/PMPA5/SCK2) .1 μF 47 μF (RG7/PMPA4/SDI2) R19 50 52 (RG8/PMPA3/SD02) \$10K R20 RG9/PMPA2/SS2 ≶10K RD9 RD8 +<u>3.3</u>V LM1117 58 60 RD11/PMPCS1 +5V R15 NAND\_CS 61 63 C27 C28 65 67 69 71 73 66 47 μF D2 68 70 +3.3V R17 RG7/PMPA4/SDI2 72 74 470 ≥ RG6/PMPA5/SCK2 76 .1 μF TC1264-1.8VDBTR +1.8V SST25VF016B-50-4C-S2AF +3.3∨ -83 C29 22 μF 90 92 94 102 (RB10/PMPA13) (RB11/PMPA12) 104 106 107 108 109 110 113 114 115 116 117 118 119 120 (RB14/PMPA1) 132 Hirose Electronic Co Ltd FX10A-120S/12-SV(71)

# **Getting Started (Continued)**

Library for availability.

to program and run demonstration projects.

The Graphics LCD Controller PICtail™ Plus SSD1926 Board can be used in conjunction with the Microchip Graphics Library. The Microchip Graphics Library and other firmware examples can be downloaded from <a href="http://www.microchip.com/graphics">http://www.microchip.com/graphics</a>. The Microchip Graphics Library installer will create a **Start Menu** folder under Programs\Microchip\Graphics Library vX.XX. Please refer to the "**Getting Started**" section in the Microchip Graphics Library Help at this location

The Graphics LCD Controller PICtail™ Plus SSD1926 Board has a footprint for NAND Flash memory (U2). End designers may choose to install a NAND Flash (i.e., MT29F2G08AADWP). In which case, some modifications must be done. Resistor, R15, should be removed and a zero ohm resistor, R18, should be installed to switch the chip select signal from the serial Flash to the NAND Flash. Software support for the NAND Flash and SD/MMC card is not available at initial release of hardware. Please check the current release of the Microchip Graphics

A different display board may also be available, please check <a href="https://www.microchip.com/graphics">www.microchip.com/graphics</a> for available options. If an end designer chooses to develop a custom display board, the included schematic shows the details of signal connections. Do note that a different display may require modifications to the software provided with the Microchip Graphics Library to function properly. Also refer to the SSD1926 Data Sheet, Table 6-10, as different interface modes may require different signals mapping.

**Important:** It is the user's responsibility to obtain a copy of, familiarize themselves fully with, and comply with the requirements and licensing obligations applicable to third party tools, systems and/or specifications. This includes, but is not limited to, Flash-based media and FAT file systems available from SD Card Association, MultiMediaCard Association and Microsoft® Corporation.

# **Signal Interface for Display Connector**

Pin No.	Symbol	Level	Description
A1, B1	+9V	+9.0V	Power Supply
A2, B2	GND	GND	Ground
A3, A4	+3.3V	+3.3V	Power Supply
B3, B4	+5V	+5.0V	Power Supply
A5, B5	GND	GND	Ground
A6	LEFT/X-	I/O	Touch Panel Left
B6	TOP/Y-	I/O	Touch Panel Top
A7	RIGHT/X+	I/O	Touch Panel Right
B7	BOTTOM/Y+	I/O	Touch Panel Bottom
A8	PEN_INT	I	Pen Interrupt (Touch Panel Driver)
B8	SENSE	I	5-Wire Touch Panel Sense
A9	SCK	0	PIC® Microcontroller SCK
B9	SDO	0	PIC Microcontroller SDO
A10	SDI	I	PIC Microcontroller SDI
B10	SS	0	PIC Microcontroller SS
A11	BKLHT_EN	0	Enable for Backlight Driver
B11	BKLHT_PWM	0	PWM Output for Backlight Driver
A12, B12	GND	GND	Ground
A13	SHIFT	0	Pixel Shift Signal
B13	DEN	0	Data Enable for 24-Bit Digital RGB Interface
A14	FRAME	0	Frame Pulse
B14	LINE	0	Line Pulse
A15-A26, B15-B26	LDATA0-LDATA23	0	24-Bit Data
A27	GPO (DISP_ON)	0	GPO can be Configured for Display On or as General Purpose Output
B27	GPIO0 (CS)	I/O	GPIO0 can be Used for SPI CS or as General Purpose I/O
A28	GPIO1 (SCK)	I/O	GPIO1 can be Used for SPI SCK or as General Purpose I/O
B28	GPIO2 (MOSI)	I/O	GPIO2 can be Used for SPI MOSI or as General Purpose I/O
A29	GPIO3 (MISO)	I/O	GPIO3 can be Used for SPI MISO or as General Purpose I/O
A30-A31, B29-B31	NC	_	Not Connected
A32, B32	GND	GND	Ground