



NPN SILICON EPITAXIAL PLANAR TRANSISTORS

BC546_BC550





For switching and AF amplifier application

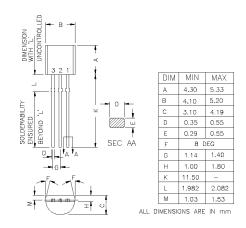
ABSOLUTE MAXIMUM RATINGS (T_a=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	BC546	BC547	BC550	BC548	BC549	
Collector Base Voltage	V _{CBO}	80	50		30		V
Collector Emitter Voltage	V _{CEO}	65	45	45		30	
Emitter Base Voltage	V _{EBO} 6						V
Collector Current (DC)	Ι _C	100					mA
Collector Current - Peak	I _{CM}	I _{CM} 200					
Power Dissipation	P _{tot}	P _{tot} 500					
Storage Temperature	T _{stg}	- 65 to +150					°C
Junction Temperature	T _j 150						°C

Characteristics at Ta = 25°C

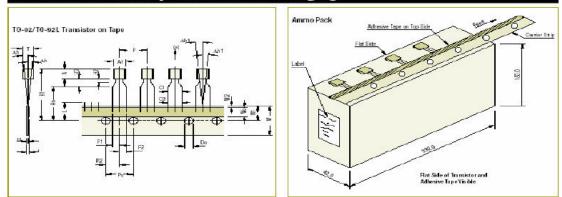
DESCRIPTION	SYMBOL	TEST CONDITION	MIN	MAX	UNITS
		I _C =2mA, V _{CE} =5V	75 110	800 220	
DC Current Gain	h _{FE}	A B C	200 420	450 800	
Collector Emitter Coturation Voltors		I _C =10mA, I _B =0.5mA	-	0.25	V
Collector Emitter Saturation Voltage	V _{CE(Sat)}	I _C =100mA, I _B =5mA	-	0.60	V
Base Emitter on Voltage	V	I _C =2mA, V _{CE} =5V	0.55	0.70	V
Base Ellitter on Voltage	$V_{BE(on)}$	I _C =10mA, V _{CE} =5V	-	0.77	V
Collector Base Cut off Current	I _{CBO}	$V_{CB}=30V, I_{E}=0$	-	15	nA
Emitter Base Cut off Current	I _{EBO}	V _{EB} =5V	-	100	nA
Collector Base Breakdown Voltage					
BC546		1 100.04	80	-	
BC547 , BC550		Ι _C =100μΑ	50	-	V
BC548 , BC549			30	-	
Collector Emitter Breakdown Voltage					
BC546		I _c =2mA	65	-	
BC547 , BC550		1 _C =2111A	45	-	V
BC548 , BC549			30	-	
Emitter Base Breakdown Voltage	V _{(BR)EBO}	I _E =10μA	6	-	V
Transition Frequency	f _T	I_C =10mA, V_{CE} =5V,f=100MHz	100	-	MHz
Collector Base Capacitance	C _{cb}	V _{CB} =10V, f=1MHz	-	6.0	pF

BC546_550Rev_6 231112E



Packaging Specifications										
T & A: Tape and Ammo Pack;	T & R: Tape and Real; Bulk: I	Loose in PolyBags; Tube:	: Tube and Carto	n; K : 1,000						
Package / Case Type	Packaging Type	Std. Packing		Inner Carton		Outer Carton				
		Oty	Oty	Size L x W x H	Gross Weight	Qty	SizeLxWxH	Gross Weight		
				(cm)	(Kg)		(cm)	(Kg)		
TO-92	Bulk	1,000	5K	19 x 19 x 8	1.1	80K	43 x 40 x 35	20.0		
	T&A	2,000	2K	32 x 4.5 x 20	0.7	40K	43 x 40 x 35	15.2		

TO-92 and TO-92L Tape and Ammo Packaging



Tape Specifications

		T0-92				T0-92L				Taping Specification
Item description	Symbol	Min	Nom	Max	Tol	Min	Nom	Max	Tol	 Maximum alignment deviation betwee leads not to be greater than 0.20 mm
Body width	Δ1	4.45		5.20		4.7		5.1		 Maximum non-cumulative variation
Body height	A	4.32		5.33	1.1	7.8	3	8.2		 Maximum for contrative variation between tape feed holes shall not ex
Body thickness	T	3.18		4.19		3.7		4.1		1 mm in 20 pitches.
Pitch of component ^{PI}	P		12.7		±1.0		12.7		±0.3	[1] · · · · · · · · · · · · · · · · · · ·
Feed hole pitch ⁵¹	Po	2	12.7		±0.3		12.7	2	±0.2	 Hold down tape not to exceed beyon edge(s) carrier tape and there shall be
Feed hole center to component centre ⁵²	P2		6.35		±0.4		6.35		±0.3	exposure of adhesive.
Comp. alignment, Side view ⁶³	Dh		0	1.0			0		±1.0	 No more than 3 consecutive missing
Comp. alignment, Front view ^{\$3}	Dht		0	1.3			0		±1.0	components is permitted.
Tape width ^{or}	W	2	18		±0.5		18.0	2	+1.0 -0.5	 A tape trailer, having at least three
Hold down tape width ^{or}	Wo		6		+0.2		6.0		+0.5	feed holes is required after the last
Hole position	W1		9		+0.7 -0.5		9.0		±0.5	compenent.
Hold-down tape position	W2	0.0		0.7				1.0		 Splices shall not interfere with the
Lead wire clinch height	Ho	3	16		±0.5		16.0	5	±0.5	sprocket feed holes.
Component height	H1			24.0				29.0		
Length of snipped leads	L			11.0				11.0		
Feed hole diameter ^{Cr}	Do	2	4		±0.2	2	4.0	2	±0.2	
Total tape thickness ⁵⁴	t			1.2			0.2		±0.5	
Lead-to-lead distance ^{Cr}	F1,F2	2.4		2.7		2.2	1	2.0		§1 Cumulative pitch error 1.0 mm/20 pitel
Stand off	H2	0.45		1.45		0.45		1.45		§2 To be measured at bottom of clinch.
Clinch height	H3		1 2	3.0	10		1	4.0		§3. At top of body.
Lead parallelismGr	CI-GZ			0.22				0.22		§4 11 = 0.3 - 0.5 mm
Pull-out force	(p)	6N				GN				Cr Critical Dimension.

- Neien m.
- mened
- nd the be no

BC546_550Rev_6 231112E

TO-92 Plastic Package

Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



CDIL is a registered Trademark of Continental Device India Limited C-120 Naraina Industrial Area, New Delhi 110 028, India. Telephone + 91-11-2579 6150, 4141 1112 Fax + 91-11-2579 5290, 4141 1119 email@cdil.com www.cdil.com

BC546_550Rev_6 231112E