SN54365A THRU SN54368A, SN54LS365A THRU SN54LS368A SN74365A THRU SN74368A, SN74LS365A THRU SN74LS368A HEX BUS DRIVERS WITH 3-STATE OUTPUTS

DECEMBER 1983-REVISED MARCH 1988

- 3-State Outputs Drive Bus Lines or Buffer Memory Address Registers
- Choice of True or Inverting Outputs
- Package Options Include Plastic "Small Outline" Packages, Ceramic Chip Carriers and Flat Packages, and Plastic and Ceramic DIPs
- Dependable Texas Instruments Quality and Reliability

'365A, '367A, 'LS365A, 'LS367A True Outputs '366A, '368A, 'LS366A, 'LS368A Inverting Outputs

description

These Hex buffers and line drivers are designed specifically to improve both the performance and density of three-state memory address drivers, clock drivers, and bus oriented receivers and transmitters. The designer has choice of selected combinations of inverting and noninverting outputs, symmetrical \overline{G} (active-low control) inputs.

These devices feature high fan-out, improved fan-in, and can be used to drive terminated lines down to 133 ohms.

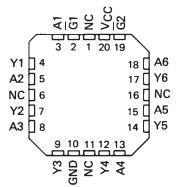
The SN54365A thru SN54368A and SN54LS365A thru SN54LS368A are characterized for operation over the full military temperature range of -55 °C to 125 °C. The SN74365A thru SN74368A and SN74LS365A thru SN74LS368A are characterized for operation from 0 °C to 70 °C.

SN54365A, 366A, SN54LS365A, 366A... J PACKAGE SN74365A, 366A... N PACKAGE SN74LS365A, SN74LS366A... D OR N PACKAGE

(TOP VIEW)

G1 [1	U_{16}	D⊻cc
A1 [2	15] G2
Y1 [3	14	A 6
A2 [4	13] Y6
Y2 [5	12] A5
A3 [6	11] Y5
Y3 [7	10	A4
GND [8	9	🗋 Y4

SN54LS365A, SN54LS366A . . . FK PACKAGE (TOP VIEW)

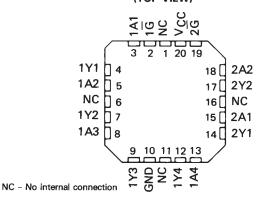


SN54367A, 368A, SN54LS367A, 368A... J PACKAGE SN74367A, 368A... N PACKAGE

SN74LS367A, SN74LS368A . . . D OR N PACKAGE (TOP VIEW)

	•			•
1G	С	1	U ₁₆]v <u>c</u> c
1A1		2	15]] 2 G
1Y1	Ц	3	14	2A2
1A2		4	13] 2Y2
1Y2		5	12	2A1
1A3		6	11]2Y1
1Y3		7	10] 1A4
GND	П	8	9] 1Y4

SN54LS367A, SN54LS368A . . . FK PACKAGE (TOP VIEW)



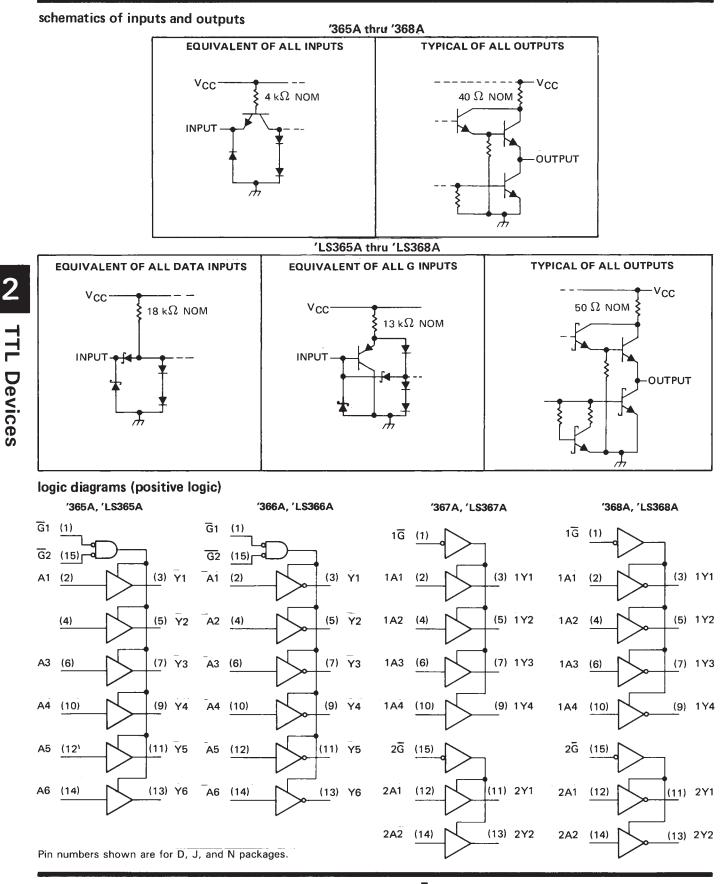
PRODUCTION DATA documents contain information current as of publication data. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.



TL Devices

2

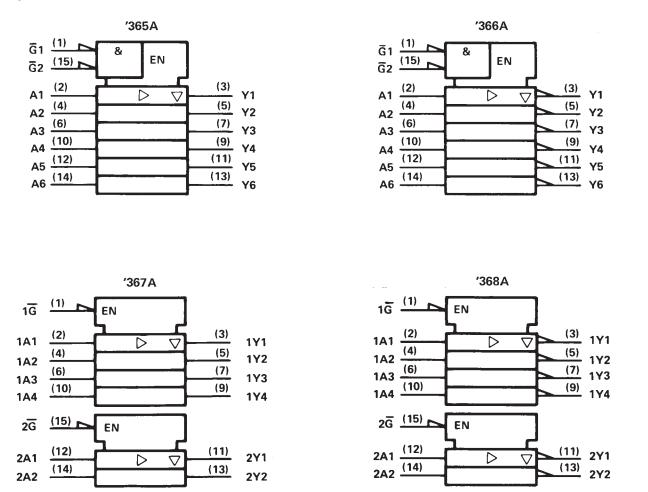
SN54365A THRU SN54368A, SN54LS365A THRU SN54LS368A SN74365A THRU SN74368A, SN74LS365A THRU SN74LS368A HEX BUS DRIVERS WITH 3-STATE OUTPUTS





SN54365A THRU SN54368A, SN54LS365A THRU SN54LS368A SN74365A THRU SN74368A, SN74LS365A THRU SN74LS368A **HEX BUS DRIVERS WITH 3-STATE OUTPUTS**

logic symbols[†]



[†]These symbols are in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12. Pin numbers shown are for D, J, and N packages.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, V _{CC} (see Note 1)	
Input voltage: '365A, '366A, '367A, '368A	5.5 V
'LS365A, 'LS366A, 'LS367A, 'LS368A	7V
Voltage applied to a disabled 3-state output	
Operating free-air temperature: SN54'	-55° C to 125° C
SN74'	\dots 0°C to 70°C
Storage temperature range	-65° C to 150° C

NOTE 1: Voltage values are with respect to network ground terminal.



SN54365A, SN54367A SN74365A, SN74367A HEX BUS DRIVERS WITH 3-STATE OUTPUTS

recommended operating conditions

			SN54365A SN74365A SN54367A SN74367A				UNIT	
		MIN	NOM	МАХ	MIN	NOM	MAX	
Vcc	Supply voltage	4.5	5	5.5	4.75	5	5.25	V
VIH	High-level input voltage	2			2			v
VIL	Low-level input voltage			0.8			0.8	v
юн	High-level output current			- 2			- 5.2	mA
IOL	Low-level output current			32			32	mA
Τ _A	Operating free-air temperature	- 55		125	0		70	°c

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER			TEST CONDITIONS†			SN54368 SN54367		s s	UNIT		
					MIN TYP‡ MAX		MIN	TYP‡	MAX]	
_ viĸ		V _{CC} = MIN,	l ₁ = – 12 mA				- 1.5			- 1.5	v
Va		V _{CC} = MIN,	V _{IH} = 2 V,	V _{IL} = 0.8 V,							
Vol	1	I _{OH} = MAX			2.4	3.3		2.4	3.1		V
Voi		V _{CC} ≈ MIN,	V _{IH} = 2 V,	V _{IL} = 0.8 V,							
0	L	I _{OL} = 32 mA					0.4			0.4	
		V _{CC} = MAX,	V _{IH} = 2 V,	V _{IL} = 0.8 V,							
		V _O = 2.4 V					40			40	
l ^I oz		V _{CC} = MAX,	V _{IH} = 2 V	V _{IL} = 0.8 V,							μA
		V _O = 0.4 V					- 40			- 40	
ι,		V _{CC} = MAX,	V _I = 5.5 V				1			1	mA
Чн		V _{CC} = MAX,	VI = 2.4 V				40			40	μΑ
	A Inputs	V _{CC} = MAX,	V ₁ = 0.5 V,	Either G input at 2 V			- 40			- 40	μA
կե	A inputs	V _{CC} = MAX,	V _I = 0.4 V,	Both \overline{G} inputs at 0.4 V			- 1.6			- 1.6	
	G Inputs	V _{CC} = MAX,	V ₁ = 0.4 V				- 1.6			- 1.6	mA
los	ş	V _{CC} = MAX			- 40	_	- 130	- 40		- 130	mA
l'cc		V _{CC} = MAX,	Data inputs = $0 V$,	Output controls = 4.5 V		65	85		65	85	mA

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

 \ddagger All typical values are at V_{CC} = 5 V, T_A = 25°C.

^tPHZ

^tPLZ

§ Not more than one output should be shorted at a time.

witching chara	acteristics, V	CC = 5 V, TA =	$^{\circ}$ 25 $^{\circ}$ C (see note 2)	
PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN
^t PLH				
^t PHL				
^t PZH	A p ₁	~	$R_{L} = 400 \Omega$, $C_{L} = 50 pF$	
^t PZL	Any	Ŷ		

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.



 $R_L = 400 \Omega$,

 $C_L = 5 pF$

TYP

MAX

16

22

35

37

11

27

UNIT

ns

ns

ns

ns

ns

ns

SN54366A, SN54368A SN74366A, SN74368A **HEX BUS DRIVERS WITH 3-STATE OUTPUTS**

recommended operating conditions

		SN54366 SN54368		SN74366A SN74368A			UNIT
	MIN	NOM	МАХ	MIN	NOM	МАХ	
V _{CC} Supply voltage	4.5	5	5.5	4.75	5	5.25	v
VIH High-level input voltage	2			2			V
VIL Low-level input voltage			0.8			0.8	V
IOH High-level output current			- 2			- 5.2	mA
IOL Low-level output current			32			32	mA
T _A Operating free-air temperature	- 55		125	0		70	°c

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PAR	AMETER		TEST CONDITION	s†		N54366 N54368			N74366 N74368		
					MIN	TYP‡	МАХ	MIN	түр‡	MAX	
Vik	ζ.	V _{CC} = MIN,	l _I = – 12 mA				- 1.5			- 1.5	V
Ň		V _{CC} = MIN,	V _{IH} = 2 V,	V _{IL} = 0.8 V,			-				
VO	Н	I _{OH} = MAX			2.4	3.3		2.4	3.1		V
		V _{CC} = MIN,	V _{IH} = 2 V,	V _{IL} ≠ 0.8 V,						~ 4	
VO	L	1 _{0L} = 32 mA					0.4			0.4	V
		V _{CC} = MAX,	V _{IH} = 2 V,	V _{IL} = 0.8 V,						4.0	_
1		V _O = 2.4 V					40			40	
loz		V _{CC} ≏ MAX,	V _{IH} = 2 V	V _{1L} = 0.8 V,			4.0			4.0	μA
	_	V _O = 0.4 V					- 40			- 40	
j.		V _{CC} = MAX,	V _I = 5.5 V				1			1	mA
ΙН		V _{CC} = MAX,	V ₁ = 2.4 V				40			40	μA
	A 1	V _{CC} = MAX,	V _I = 0.5 V,	Either G input at 2 V			- 40			- 40	μA
ηĽ	A Inputs	V _{CC} = MAX,	V ₁ = 0.4 V,	Both \overline{G} inputs at 0.4 V			- 1.6			- 1.6	mA
	G Inputs	V _{CC} = MAX,	V _I = 0.4 V				- 1.6			- 1.6	
los	§	V _{CC} = MAX			- 40		- 130	- 40		- 130	mA
Icc		V _{CC} = MAX,	Data inputs ≈ 0 V,	Output controls = 4.5 V,		59	77		59	77	mA

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

‡ All typical values are at $V_{CC} = 5 V$, $T_A = 25^{\circ}C$. § Not more than one output should be shorted at a time.

switching characteristics, V_{CC} = 5 V, $T_A = 25^{\circ}C$ (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CON	IDITIONS	ΜΙΝ ΤΥΡ	МАХ	UNIT
^t PLH						17	ns
^t PHL			D 400 o	0 - 50 - 5		16	ns
^t PZH	Any	Y	$R_{L} = 400 \Omega$,	C _L = 50 pF		35	ns
^t PZL	Any	r l				37	ns
^t PHZ			B 400 C.	0 - 5 - 5		11	ns
^t PLZ			$R_{L} = 400 \ \Omega,$	C _L = 5 pF		27	ns

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.



SN54LS365A, SN54LS367A SN74LS365A, SN74LS367A HEX BUS DRIVERS WITH 3-STATE OUTPUTS

recommended operating conditions

		54LS36 54LS36		SN74LS365A SN74LS367A			UNIT
	MIN	NOM	МАХ	MIN	NOM	МАХ	
V _{CC} Supply voltage	4.5	5	5.5	4.75	5	5.25	v
V _{IH} High-level input voltage	2			2			V
VIL Low-level input voltage			0.7			0.8	v
IOH High-level output current			- 1			- 2.6	mA
IOL Low-level output current			12			24	mA
T _A Operating free-air temperature	55		125	0		70	°c

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARA	AMETER	test conditionst				154LS36 154LS36		SI S	UNIT		
					MIN	TYP‡	МАХ	MIN	TYP‡	МАХ	
٧ıĸ		V _{CC} = MIN,	l _l = – 18 mA				- 1.5			- 1.5	v
VOI	4	V _{CC} = MIN, I _{OH} = MAX	V _{IH} ≖ 2 V,	VIL = MAX,	2.4	3.3		2.4	3.1		v
		V _{CC} = MIN, I _{OL} = 12 mA	V _{1H} = 2 V,	VIL = MAX,		0.25	0.4		0.25	0.4	v
Voi	_	V _{CC} = MIN, I _{OL} = 24 mA	V _{IH} = 2 V,	V _{IL} = 0.8 V,					0.35	0.5	
		V _{CC} = MAX, V _O = 2.4 V	V _{IH} = 2 V,	V _{IL} = MAX,			20			20	μΑ
loz		V _{CC} = MAX, V _O = 0.4 V	V _{IH} = 2 V,	V _{IL} = MAX,			- 20			- 20	μΑ
I _I		V _{CC} = MAX,	V ₁ = 7 V				0.1			0.1	mA
ЧН		V _{CC} = MAX,	V _I = 2.7 V				20			20	μA
	A Inputs	V _{CC} = MAX,	V _I = 0.5 V,	Either \overline{G} input at 2 V			- 20		_	- 20	μA
ЧL		V _{CC} = MAX,	V _I = 0.4 V,	Both \overline{G} inputs at 0.4 V			- 0.4			- 0.4	mA
	G Inputs	V _{CC} ≃ MAX,	V _I = 0.4 V				- 0.2			- 0.2	
los	§	V _{CC} = MAX			- 40		- 225	- 40		- 225	mA
^I cc		V _{CC} = MAX,	Data inputs = 0 V,	Output controls = $4.5 V$,		14	24		14	24	mA

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

 \ddagger All typical values are at V_CC = 5 V, T_A = 25 $^{\rm o}{\rm C}.$

§ Not more than one output should be shorted at a time, and the duration of the short circuit should not exceed one second.



SN54LS365A, SN54LS367A SN74LS365A, SN74LS367A HEX BUS DRIVERS WITH 3-STATE OUTPUTS

switching characteristics, $V_{CC} = 5 V$, $T_A = 25^{\circ}C$ (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CON	DITIONS	MIN TYP	MAX	UNIT
^t PLH					10	16	ns
^t PHL			B 667 O	0 - 45 - 5	9	22	ns
^t PZH	Any	Y	R _L = 667 Ω,	C _L = 45 pF	19	35	ns
^t PZL	A09	T			24	40	ns
^t PHZ			B 667.0	0 - 5 - 5		30	ns
^t PLZ			R _L = 667 Ω,	C _L = 5 pF		35	ns

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.



SN54LS366A, SN54LS368A SN74LS366A, SN74LS368A HEX BUS DRIVERS WITH 3-STATE OUTPUTS

recommended operating conditions

			N54LS36 N54LS36			N74LS30 N74LS30		
		MIN	NOM	мах	MIN	NOM	MAX	
Vcc	Supply voltage	4.5	5	5.5	4.75	5	5.25	V
VIH	High-level input voltage	2			2			V
VIL	Low-level input voltage			0.7			0.8	V
юн	High-level output current			-1			- 2.6	mA
IOL	Low-level output current			12			24	mA
Τ _A	Operating free-air temperature	- 55		125	0		70	°c

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PAR	AMETER		TEST CONDITION	st		N54LS30			174LS36		UNIT
					MIN	TYP‡	MAX	MIN	түр‡	MAX	
Vik		V _{CC} = MIN,	I _I = – 18 mA				- 1.5			- 1.5	v
Vo	н		V _{IH} = 2 V,	V _{IL} = MAX,	2.4	3.3		2.4	3.1		v
		$I_{OH} = MAX$ $V_{CC} = MIN,$ $I_{OL} = 12 mA$	V _{IH} = 2 V,	V _{IL} = MAX,		0.25	0.4		0.25	0.4	
Vo:	L	V _{CC} = MIN, I _{OL} = 24 mA	V _{IH} = 2 V,	V _{IL} = 0.8 V,					0.35	0.5	V
		V _{CC} = MAX, V _O = 2.4 V	V _{IH} = 2 V,	V _{IL} = MAX,			20			20	μA
l oz		V _{CC} = MAX, V _O = 0.4 V	V _{IH} = 2 V,	V _{IL} = MAX,			- 20			- 20	μΑ
Ц		V _{CC} = MAX,	V _I = 7 V				0.1			0.1	mA
Чн		V _{CC} = MAX,	V _I = 2.7 V				20			20	μA
	AInputs	V _{CC} = MAX,	V _I = 0.5 V,	Either G input at 2 V			- 20			- 20	uА
IIL.		V _{CC} = MAX,	V ₁ = 0.4 V,	Both \overline{G} inputs at 0.4 V			- 0.4			- 0.4	mA
	G Inputs	V _{CC} = MAX,	V ₁ = 0.4 V				- 0.2			- 0.2	
los	§	V _{CC} = MAX			- 40		- 225	- 40		- 225	mA
l cc		V _{CC} = MAX,	Data inputs = 0 V,	Output controls = 4.5 V,		12	21		12	21	mA

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

 \ddagger All typical values are at V_CC = 5 V, T_A = 25°C.

§ Not more than one output should be shorted at a time, and the duration of the short circuit should not exceed one second.



SN54LS366A, SN54LS368A SN74LS366A, SN74LS368A HEX BUS DRIVERS WITH 3-STATE OUTPUTS

switching characteristics, $V_{CC} = 5 V$, $T_A = 25^{\circ}C$ (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CON	DITIONS	MIN TYP	МАХ	UNIT
^t PLH					7	15	ns
^t PHL			P 667 O	.C _L = 45 pF	12	18	ns
^t PZH	Any	Y	$R_L = 667 \Omega$,	_CL = 45 μP	18	35	ns
^t PZL		1			28	45	ns
^t PHZ			D - 667 O			32	ns
^t PLZ			$R_{L} = 667 \Omega,$	C _L = 5 pF		35	ns

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.





24-Sep-2015

PACKAGING INFORMATION

Orderable Device	Status	Package Type	•	Pins	•	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking	Samples
	(1)		Drawing		Qty	(2)	(6)	(3)		(4/5)	
5962-9687802QEA	OBSOLETE	CDIP	J	16		TBD	Call TI	Call TI	-55 to 125		
5962-9687802QFA	OBSOLETE	CFP	W	16		TBD	Call TI	Call TI	-55 to 125		
5962-9687802QFA	OBSOLETE	CFP	W	16		TBD	Call TI	Call TI	-55 to 125		
JM38510/16303BEA	OBSOLETE	CDIP	J	16		TBD	Call TI	Call TI	-55 to 125		
JM38510/16303BEA	OBSOLETE	CDIP	J	16		TBD	Call TI	Call TI	-55 to 125		
JM38510/16304BEA	OBSOLETE	CDIP	J	16		TBD	Call TI	Call TI	-55 to 125		
JM38510/16304BEA	OBSOLETE	CDIP	J	16		TBD	Call TI	Call TI	-55 to 125		
JM38510/32201B2A	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	-55 to 125	JM38510/ 32201B2A	Samples
JM38510/32201B2A	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	-55 to 125	JM38510/ 32201B2A	Samples
JM38510/32201BEA	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	JM38510/ 32201BEA	Samples
JM38510/32201BEA	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	JM38510/ 32201BEA	Samples
JM38510/32201BFA	LIFEBUY	CFP	W	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	JM38510/ 32201BFA	
JM38510/32201BFA	LIFEBUY	CFP	W	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	JM38510/ 32201BFA	
JM38510/32203B2A	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	-55 to 125	JM38510/ 32203B2A	Samples
JM38510/32203B2A	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	-55 to 125	JM38510/ 32203B2A	Samples
JM38510/32203BEA	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	JM38510/ 32203BEA	Samples
JM38510/32203BEA	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	JM38510/ 32203BEA	Samples
JM38510/32203BFA	ACTIVE	CFP	W	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	JM38510/ 32203BFA	Samples
JM38510/32203BFA	ACTIVE	CFP	W	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	JM38510/ 32203BFA	Samples
JM38510/32203SEA	LIFEBUY	CDIP	J	16	25	TBD	A42	N / A for Pkg Type	-55 to 125	JM38510/ 32203SEA	



Orderable Device	Status	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish (6)	MSL Peak Temp	Op Temp (°C)	Device Marking (4/5)	Samples
JM38510/32203SEA	LIFEBUY	CDIP	J	16	25	TBD	A42	N / A for Pkg Type	-55 to 125	JM38510/ 32203SEA	
JM38510/32203SFA	LIFEBUY	CFP	W	16	25	TBD	A42	N / A for Pkg Type	-55 to 125	JM38510/ 32203SFA	
JM38510/32203SFA	LIFEBUY	CFP	W	16	25	TBD	A42	N / A for Pkg Type	-55 to 125	JM38510/ 32203SFA	
M38510/32201B2A	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	-55 to 125	JM38510/ 32201B2A	Samples
M38510/32201B2A	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	-55 to 125	JM38510/ 32201B2A	Samples
M38510/32201BEA	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	JM38510/ 32201BEA	Samples
M38510/32201BEA	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	JM38510/ 32201BEA	Samples
M38510/32201BFA	LIFEBUY	CFP	W	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	JM38510/ 32201BFA	
M38510/32201BFA	LIFEBUY	CFP	W	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	JM38510/ 32201BFA	
M38510/32203B2A	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	-55 to 125	JM38510/ 32203B2A	Samples
M38510/32203B2A	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	-55 to 125	JM38510/ 32203B2A	Samples
M38510/32203BEA	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	JM38510/ 32203BEA	Samples
M38510/32203BEA	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	JM38510/ 32203BEA	Samples
M38510/32203BFA	ACTIVE	CFP	W	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	JM38510/ 32203BFA	Samples
M38510/32203BFA	ACTIVE	CFP	W	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	JM38510/ 32203BFA	Samples
M38510/32203SEA	LIFEBUY	CDIP	J	16	25	TBD	A42	N / A for Pkg Type	-55 to 125	JM38510/ 32203SEA	
M38510/32203SEA	LIFEBUY	CDIP	J	16	25	TBD	A42	N / A for Pkg Type	-55 to 125	JM38510/ 32203SEA	
M38510/32203SFA	LIFEBUY	CFP	W	16	25	TBD	A42	N / A for Pkg Type	-55 to 125	JM38510/ 32203SFA	



Orderable Device	Status	Package Type	Package	Pins	Package	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking	Samples
	(1)		Drawing		Qty	(2)	(6)	(3)		(4/5)	
M38510/32203SFA	LIFEBUY	CFP	W	16	25	TBD	A42	N / A for Pkg Type	-55 to 125	JM38510/ 32203SFA	
SN54365AJ	OBSOLETE	CDIP	J	16		TBD	Call TI	Call TI	-55 to 125		
SN54365AJ	OBSOLETE	CDIP	J	16		TBD	Call TI	Call TI	-55 to 125		
SN54366AJ	OBSOLETE	CDIP	J	16		TBD	Call TI	Call TI	-55 to 125	SN54366AJ	
SN54366AJ	OBSOLETE	CDIP	J	16		TBD	Call TI	Call TI	-55 to 125	SN54366AJ	
SN54367AJ	OBSOLETE	CDIP	J	16		TBD	Call TI	Call TI	-55 to 125		
SN54367AJ	OBSOLETE	CDIP	J	16		TBD	Call TI	Call TI	-55 to 125		
SN54368AJ	OBSOLETE	CDIP	J	16		TBD	Call TI	Call TI	-55 to 125		
SN54368AJ	OBSOLETE	CDIP	J	16		TBD	Call TI	Call TI	-55 to 125		
SN54LS365AJ	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	SN54LS365AJ	Samples
SN54LS365AJ	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	SN54LS365AJ	Samples
SN54LS366AJ	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	SN54LS366AJ	Samples
SN54LS366AJ	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	SN54LS366AJ	Samples
SN54LS367AJ	ACTIVE	CDIP	J	16	25	TBD	A42	N / A for Pkg Type	-55 to 125	SN54LS367AJ	Samples
SN54LS367AJ	ACTIVE	CDIP	J	16	25	TBD	A42	N / A for Pkg Type	-55 to 125	SN54LS367AJ	Samples
SN54LS368AJ	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	SN54LS368AJ	Samples
SN54LS368AJ	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	SN54LS368AJ	Samples
SN74365AN	OBSOLETE	PDIP	Ν	16		TBD	Call TI	Call TI	0 to 70		
SN74365AN	OBSOLETE	PDIP	N	16		TBD	Call TI	Call TI	0 to 70		
SN74366AN	OBSOLETE	PDIP	Ν	16		TBD	Call TI	Call TI	0 to 70		
SN74366AN	OBSOLETE	PDIP	N	16		TBD	Call TI	Call TI	0 to 70		
SN74367AN	OBSOLETE	PDIP	Ν	16		TBD	Call TI	Call TI	0 to 70		
SN74367AN	OBSOLETE	PDIP	Ν	16		TBD	Call TI	Call TI	0 to 70		
SN74367AN3	OBSOLETE	PDIP	N	16		TBD	Call TI	Call TI	0 to 70		
SN74367AN3	OBSOLETE	PDIP	N	16		TBD	Call TI	Call TI	0 to 70		
SN74368AN	OBSOLETE	PDIP	N	16		TBD	Call TI	Call TI	0 to 70		
SN74368AN	OBSOLETE	PDIP	N	16		TBD	Call TI	Call TI	0 to 70		



Orderable Device	Status	Package Type	Package	Pins		Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking	Sampl
	(1)		Drawing		Qty	(2)	(6)	(3)		(4/5)	
SN74368AN3	OBSOLETE	PDIP	N	16		TBD	Call TI	Call TI	0 to 70		
SN74368AN3	OBSOLETE	PDIP	Ν	16		TBD	Call TI	Call TI	0 to 70		
SN74LS365AD	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	0 to 70	LS365A	Samp
SN74LS365AD	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	0 to 70	LS365A	Samp
SN74LS365ADR	ACTIVE	SOIC	D	16	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	0 to 70	LS365A	Samp
SN74LS365ADR	ACTIVE	SOIC	D	16	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	0 to 70	LS365A	Samp
SN74LS365AN	ACTIVE	PDIP	Ν	16	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	0 to 70	SN74LS365AN	Samp
SN74LS365AN	ACTIVE	PDIP	Ν	16	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	0 to 70	SN74LS365AN	Samp
SN74LS365AN3	OBSOLETE	PDIP	Ν	16		TBD	Call TI	Call TI	0 to 70		
SN74LS365AN3	OBSOLETE	PDIP	Ν	16		TBD	Call TI	Call TI	0 to 70		
SN74LS365ANSR	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	0 to 70	74LS365A	Samj
SN74LS365ANSR	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	0 to 70	74LS365A	Samp
SN74LS366AD	OBSOLETE	SOIC	D	16		TBD	Call TI	Call TI	0 to 70		
SN74LS366AD	OBSOLETE	SOIC	D	16		TBD	Call TI	Call TI	0 to 70		
SN74LS366ADR	OBSOLETE	SOIC	D	16		TBD	Call TI	Call TI	0 to 70		
SN74LS366ADR	OBSOLETE	SOIC	D	16		TBD	Call TI	Call TI	0 to 70		
SN74LS366AN	OBSOLETE	PDIP	N	16		TBD	Call TI	Call TI	0 to 70		
SN74LS366AN	OBSOLETE	PDIP	N	16		TBD	Call TI	Call TI	0 to 70		
SN74LS367AD	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	0 to 70	LS367A	Samj
SN74LS367AD	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	0 to 70	LS367A	Sam
SN74LS367ADR	ACTIVE	SOIC	D	16	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	0 to 70	LS367A	Sam
SN74LS367ADR	ACTIVE	SOIC	D	16	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	0 to 70	LS367A	Sam



Orderable Device		Package Type		Pins		Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking	Samp
	(1)		Drawing		Qty	(2)	(6)	(3)		(4/5)	
SN74LS367AJ	OBSOLETE	CDIP	J	16		TBD	Call TI	Call TI	0 to 70		
SN74LS367AJ	OBSOLETE	CDIP	J	16		TBD	Call TI	Call TI	0 to 70		
SN74LS367AN	ACTIVE	PDIP	Ν	16	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	0 to 70	SN74LS367AN	Samp
SN74LS367AN	ACTIVE	PDIP	Ν	16	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	0 to 70	SN74LS367AN	Samp
SN74LS367AN3	OBSOLETE	PDIP	Ν	16		TBD	Call TI	Call TI	0 to 70		
SN74LS367AN3	OBSOLETE	PDIP	N	16		TBD	Call TI	Call TI	0 to 70		
SN74LS367ANE4	ACTIVE	PDIP	N	16	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	0 to 70	SN74LS367AN	Samj
SN74LS367ANE4	ACTIVE	PDIP	Ν	16	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	0 to 70	SN74LS367AN	Samj
SN74LS367ANSR	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	0 to 70	74LS367A	Samj
SN74LS367ANSR	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	0 to 70	74LS367A	Sam
SN74LS368AD	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	0 to 70	LS368A	Sam
SN74LS368AD	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	0 to 70	LS368A	Sam
SN74LS368ADG4	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	0 to 70	LS368A	Sam
SN74LS368ADG4	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	0 to 70	LS368A	Sam
SN74LS368AJ	OBSOLETE	CDIP	J	16		TBD	Call TI	Call TI	0 to 70		
SN74LS368AJ	OBSOLETE	CDIP	J	16		TBD	Call TI	Call TI	0 to 70		
SN74LS368AN	ACTIVE	PDIP	N	16	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	0 to 70	SN74LS368AN	Sam
SN74LS368AN	ACTIVE	PDIP	N	16	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	0 to 70	SN74LS368AN	Sam
SN74LS368AN3	OBSOLETE	PDIP	N	16		TBD	Call TI	Call TI	0 to 70		
SN74LS368AN3	OBSOLETE	PDIP	N	16		TBD	Call TI	Call TI	0 to 70		
SN74LS368ANE4	ACTIVE	PDIP	N	16	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	0 to 70	SN74LS368AN	Sam



Orderable Device	Status	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish (6)	MSL Peak Temp	Op Temp (°C)	Device Marking (4/5)	Sample
SN74LS368ANE4	ACTIVE	PDIP	N	16	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	0 to 70	SN74LS368AN	Sample
SN74LS368ANSR	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	0 to 70	74LS368A	Sample
SN74LS368ANSR	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	0 to 70	74LS368A	Sample
SNJ54365AJ	OBSOLETE	CDIP	J	16		TBD	Call TI	Call TI	-55 to 125		
SNJ54365AJ	OBSOLETE	CDIP	J	16		TBD	Call TI	Call TI	-55 to 125		
SNJ54366AJ	OBSOLETE	E CDIP	J	16		TBD	Call TI	Call TI	-55 to 125	5962-9687802QE A SNJ54366AJ	
SNJ54366AJ	OBSOLETE	CDIP	J	16		TBD	Call TI	Call TI	-55 to 125	5962-9687802QE A SNJ54366AJ	
SNJ54366AW	OBSOLETE	E CFP	W	16		TBD	Call TI	Call TI	-55 to 125	5962-9687802QF A SNJ54366AW	
SNJ54366AW	OBSOLETE	CFP	W	16		TBD	Call TI	Call TI	-55 to 125	5962-9687802QF A SNJ54366AW	
SNJ54367AJ	OBSOLETE	CDIP	J	16		TBD	Call TI	Call TI	-55 to 125		
SNJ54367AJ	OBSOLETE	CDIP	J	16		TBD	Call TI	Call TI	-55 to 125		
SNJ54367AW	OBSOLETE	CFP	W	16		TBD	Call TI	Call TI	-55 to 125		
SNJ54367AW	OBSOLETE	CFP	W	16		TBD	Call TI	Call TI	-55 to 125		
SNJ54368AJ	OBSOLETE	CDIP	J	16		TBD	Call TI	Call TI	-55 to 125		
SNJ54368AJ	OBSOLETE	CDIP	J	16		TBD	Call TI	Call TI	-55 to 125		
SNJ54368AW	OBSOLETE	CFP	W	16		TBD	Call TI	Call TI	-55 to 125		
SNJ54368AW	OBSOLETE	CFP	W	16		TBD	Call TI	Call TI	-55 to 125		
SNJ54LS365AFK	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	-55 to 125	SNJ54LS 365AFK	Sampl
SNJ54LS365AFK	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	-55 to 125	SNJ54LS 365AFK	Sampl
SNJ54LS365AJ	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	SNJ54LS365AJ	Sampl
SNJ54LS365AJ	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	SNJ54LS365AJ	Sampl



24-Sep-2015

Orderable Device	Status	Package Type	Package	Pins		Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking	Samples
	(1)		Drawing		Qty	(2)	(6)	(3)		(4/5)	
SNJ54LS365AW	LIFEBUY	CFP	W	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	SNJ54LS365AW	
SNJ54LS365AW	LIFEBUY	CFP	W	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	SNJ54LS365AW	
SNJ54LS366AFK	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	-55 to 125	SNJ54LS 366AFK	Samples
SNJ54LS366AFK	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	-55 to 125	SNJ54LS 366AFK	Samples
SNJ54LS366AJ	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	SNJ54LS366AJ	Samples
SNJ54LS366AJ	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	SNJ54LS366AJ	Samples
SNJ54LS366AW	LIFEBUY	CFP	W	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	SNJ54LS366AW	
SNJ54LS366AW	LIFEBUY	CFP	W	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	SNJ54LS366AW	
SNJ54LS367AFK	LIFEBUY	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	-55 to 125	SNJ54LS 367AFK	
SNJ54LS367AFK	LIFEBUY	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	-55 to 125	SNJ54LS 367AFK	
SNJ54LS367AJ	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	SNJ54LS367AJ	Samples
SNJ54LS367AJ	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	SNJ54LS367AJ	Samples
SNJ54LS367AW	LIFEBUY	CFP	W	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	SNJ54LS367AW	
SNJ54LS367AW	LIFEBUY	CFP	W	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	SNJ54LS367AW	
SNJ54LS368AFK	LIFEBUY	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	-55 to 125	SNJ54LS 368AFK	
SNJ54LS368AFK	LIFEBUY	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	-55 to 125	SNJ54LS 368AFK	
SNJ54LS368AJ	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	SNJ54LS368AJ	Samples
SNJ54LS368AJ	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	SNJ54LS368AJ	Samples
SNJ54LS368AW	ACTIVE	CFP	W	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	SNJ54LS368AW	Samples
SNJ54LS368AW	ACTIVE	CFP	W	16	1	TBD	A42	N / A for Pkg Type	-55 to 125	SNJ54LS368AW	Samples

⁽¹⁾ The marketing status values are defined as follows: **ACTIVE:** Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.



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24-Sep-2015

PREVIEW: Device has been announced but is not in production. Samples may or may not be available. **OBSOLETE:** TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes. **Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽³⁾ MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

⁽⁴⁾ There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(⁶⁾ Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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OTHER QUALIFIED VERSIONS OF SN54365A, SN54366A, SN54367A, SN54368A, SN54LS365A, SN54LS366A, SN54LS367A, SN54LS367A-SP, SN54LS368A, SN74365A, SN74366A, SN74366A, SN74365A, SN74366A, SN74365A, SN74366A, SN74365A, SN74366A, SN74LS365A, SN74LS366A, SN74LS366A, SN74LS368A :

• Catalog: SN74365A, SN74366A, SN74367A, SN74368A, SN74LS365A, SN74LS366A, SN74LS367A, SN54LS367A, SN74LS368A

• Military: SN54365A, SN54366A, SN54367A, SN54368A, SN54LS365A, SN54LS366A, SN54LS367A, SN54LS368A

• Space: SN54LS367A-SP



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24-Sep-2015

NOTE: Qualified Version Definitions:

- Catalog TI's standard catalog product
- Military QML certified for Military and Defense Applications
- Space Radiation tolerant, ceramic packaging and qualified for use in Space-based application

PACKAGE MATERIALS INFORMATION

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Texas Instruments

TAPE AND REEL INFORMATION





QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal												
Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
SN74LS365ADR	SOIC	D	16	2500	330.0	16.4	6.5	10.3	2.1	8.0	16.0	Q1
SN74LS365ANSR	SO	NS	16	2000	330.0	16.4	8.2	10.5	2.5	12.0	16.0	Q1
SN74LS367ADR	SOIC	D	16	2500	330.0	16.4	6.5	10.3	2.1	8.0	16.0	Q1
SN74LS368ANSR	SO	NS	16	2000	330.0	16.4	8.2	10.5	2.5	12.0	16.0	Q1

TEXAS INSTRUMENTS

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PACKAGE MATERIALS INFORMATION

2-Sep-2015



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
SN74LS365ADR	SOIC	D	16	2500	333.2	345.9	28.6
SN74LS365ANSR	SO	NS	16	2000	367.0	367.0	38.0
SN74LS367ADR	SOIC	D	16	2500	333.2	345.9	28.6
SN74LS368ANSR	SO	NS	16	2000	367.0	367.0	38.0

J (R-GDIP-T**) 14 LEADS SHOWN

CERAMIC DUAL IN-LINE PACKAGE



NOTES: A. All linear dimensions are in inches (millimeters).

- B. This drawing is subject to change without notice.
- C. This package is hermetically sealed with a ceramic lid using glass frit.
- D. Index point is provided on cap for terminal identification only on press ceramic glass frit seal only.
- E. Falls within MIL STD 1835 GDIP1-T14, GDIP1-T16, GDIP1-T18 and GDIP1-T20.

W (R-GDFP-F16)

CERAMIC DUAL FLATPACK



- NOTES: A. All linear dimensions are in inches (millimeters).
 - B. This drawing is subject to change without notice.
 - C. This package can be hermetically sealed with a ceramic lid using glass frit.
 - D. Index point is provided on cap for terminal identification only.
 - E. Falls within MIL STD 1835 GDFP2-F16



LEADLESS CERAMIC CHIP CARRIER

FK (S-CQCC-N**) 28 TERMINAL SHOWN



NOTES: A. All linear dimensions are in inches (millimeters).

B. This drawing is subject to change without notice.

- C. This package can be hermetically sealed with a metal lid.
- D. Falls within JEDEC MS-004



N (R-PDIP-T**)

PLASTIC DUAL-IN-LINE PACKAGE

16 PINS SHOWN



NOTES:

- A. All linear dimensions are in inches (millimeters).B. This drawing is subject to change without notice.
- Falls within JEDEC MS-001, except 18 and 20 pin minimum body length (Dim A).
- \triangle The 20 pin end lead shoulder width is a vendor option, either half or full width.



D (R-PDSO-G16)

PLASTIC SMALL OUTLINE



NOTES: A. All linear dimensions are in inches (millimeters).

- B. This drawing is subject to change without notice.
- Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.006 (0,15) each side.
- Body width does not include interlead flash. Interlead flash shall not exceed 0.017 (0,43) each side.
- E. Reference JEDEC MS-012 variation AC.



4211283-4/E 08/12

D (R-PDSO-G16) PLASTIC SMALL OUTLINE Stencil Openings (Note D) Example Board Layout (Note C) –16x0,55 -14x1,27 -14x1,27 16x1,50 5,40 5.40 Example Non Soldermask Defined Pad Example Pad Geometry (See Note C) 0,60 .55 Example 1. Solder Mask Opening (See Note E) -0,07 All Around

NOTES: A. All linear dimensions are in millimeters.

- B. This drawing is subject to change without notice.
- C. Publication IPC-7351 is recommended for alternate designs.
- D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525 for other stencil recommendations.
 E. Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.



MECHANICAL DATA

PLASTIC SMALL-OUTLINE PACKAGE

0,51 0,35 ⊕0,25⊛ 1,27 8 14 0,15 NOM 5,60 8,20 5,00 7,40 \bigcirc Gage Plane ₽ 0,25 7 1 1,05 0,55 0°-10° Δ 0,15 0,05 Seating Plane — 2,00 MAX 0,10PINS ** 14 16 20 24 DIM 10,50 10,50 12,90 15,30 A MAX A MIN 9,90 9,90 12,30 14,70 4040062/C 03/03

NOTES: A. All linear dimensions are in millimeters.

NS (R-PDSO-G**)

14-PINS SHOWN

- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15.



IMPORTANT NOTICE

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